



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/878,554

For: **Efficiency of Fault Simulation By
Logic Backtracing**Applicants: **Xinghao Chen, et al.**Filing Date: **June 11, 2001**Art Group: **2133**Examiner: **Joseph D. Torres**Attorney Docket: **700693.4011**Customer No.: **34313**Confirmation No.: **5168**

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

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AMENDMENT AFTER FINAL

Sir:

This paper is responsive to the Final Office Action mailed on November 1, 2004. Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims, which begin on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.